

Overview

W5500 IPRAW mode can handle IP layer's upper protocol in the TCP/IP Layer. W5500 IPRAW mode supports transport layer protocol such as [ICMP\(0x01\)](#), [IGMP\(0x02\)](#), [TCP \(0x06\)](#) and [UDP\(0x11\)](#) according to the protocol field of IP header, depending on the protocol number. The 'Ping' of ICMP is already implemented in W5500 as the Hardwired but when the user needs, the host can directly process the ICMP function include ping by opening the Socket n as an IPRAW mode.

Development Environment

- MCU : STM32F103C8
- Used program:
 - ColIDE V1.7.4
 - Flash Loader Demonstrator
 - Terminal v1.9b
 - WireShark V1.10.3

Application note & Source code

APPLICATION note

Version	Date	Download
1.0.0	2014-02-21	W5500_AP_IPRAW_V100E.pdf W5500_AP_IPRAW_V100K.pdf
1.1.0	2014-04-09	W5500_AP_IPRAW_V110E.pdf W5500_AP_IPRAW_V110K.pdf

APPLICATION Source Code

Version	Date	Download
1.0.0	2014-02-21	W5500_APC_IPRAW_V100.zip

Reference Video(Test Result)

- YouTube : [IPRAW Ping Test](#)

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For more information [W5500](#) chip please also refer to the chip's datasheet:

Datasheet

- [W5500 Datasheet v1.0.4 - English](#)
- [W5500 Datasheet v1.0.4 - Korean](#)

Datasheet History

Version	Date	Description
1.0.0	2013-08-01	Initial Release
1.0.1	2013-09-13	Corrected duplicated statements and typing errors (P.14, 23, 24, 28, 39, 51) Corrected descriptions (P.35)
1.0.2	2013-11-14	Changed " <i>descriptions of pin at 1.1 Pin Descriptions</i> " (P.10) starting "It must be tied to GND to NC (PIN38..42)" / 2. corrected typing error: starting "0x02 to 0x42 value of SOCK_MACRAW at 4.2 Socket Registers(P.50)"
1.0.3	2014-05-29	Corrected " <i>Sn_MSSR at 4.2 Socket Register</i> " (P.53): wrong descriptions of Sn_MSSR about FMTU/MTU
1.0.4	2014-06-13	1. Added Note about reading size register value (P.56, 58) / 2. Added IR Reflow Temperature Profile (P.66)

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