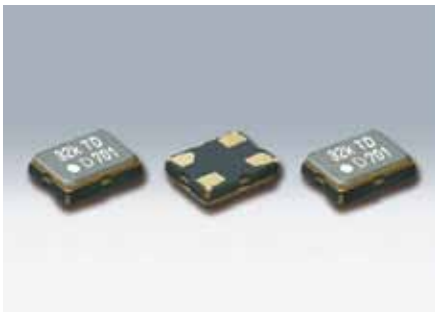
SMD TCXO

DSK321STD



Actual size

■ Features

- Digital temperature compensated type
- High precision: ±5.0×10^{-6} (−40 to +85℃)
  ±3.8×10^{-6} (−10 to +60℃)
- Low current consumption
- AEC-Q100/AEC-Q200 compliant

■ Applications

- High precision clock source
- High precision clock source for RTC

Pb-Free

RoHS Compliant

■ Standard Specification

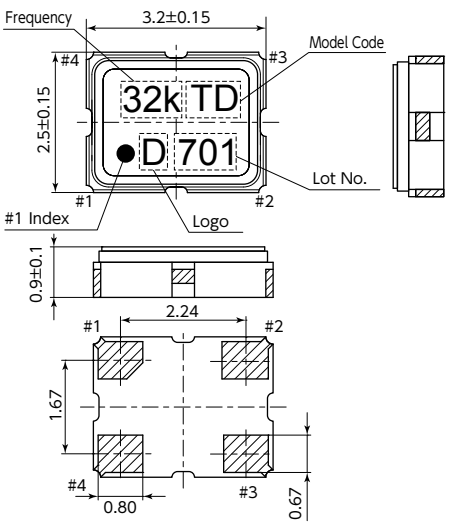
Item	Legend	Spec.				Condition
		min.	typ.	max.	Unit	
Output Frequency	f_0	–	32.768	–	kHz	
Supply Voltage Range	Vcc	+2.0	–	+5.5	V	(Temperature Compensated Operating)
		+1.3	–	+5.5		(Clock Timing Operating)
Frequency Tolerance	f_tol	−5.0	–	+5.0	×10^{-6}	−40 to +85℃
		−3.8	–	+3.8		−10 to +60℃
Current Consumption	Icc	–	+1.2	+2.5	μA	Vcc=+3.3V, Temperature Compensation Interval:0.5s, No Load
		–	+1.7	+3.2		Vcc=+5.0V, Temperature Compensation Interval:0.5s, No Load
		–	+1.0	+2.0		Vcc=+3.3V, Temperature Compensation Interval:2.0s, No Load
		–	+1.5	+3.0		Vcc=+5.0V, Temperature Compensation Interval:2.0s, No Load
Symmetry	SYM	40	50	60	%	at 50% Vcc
0 Level Output Voltage	V_{OL}	–	–	+0.4	V	
1 Level Output Voltage	V_{OH}	Vcc-0.4	–	–		
Rise and Fall Time	tr, tf	–	–	50	ns	Vcc=+2.0 to +5.5V, 10 to 90% Vcc Level
		–	–	200		Vcc=+1.3 to +5.5V, 10 to 90% Vcc Level
Load Condition	L_{CMOS}	–	–	15	pF	
Start Up Time	Tstart	–	–	3.0	s	
Packing Unit (1)	2000pcs./reel (φ180)					

(1) Moisture prevention packing is unnecessary.
Moisture Sensitivity Level: Level 1 (IPC/JEDEC J-STD-033)

Consult our sales representative for other specifications.

[mm]

■ Dimensions



Pin Connections

Pin No.	Connection
#1	Vcc
#2	GND
#3	Output
#4	Vcc

■ Recommended Land Pattern

<Top View>