## **Constant Current Regulator** & LED Driver 45 V, 25 mA ± 10%

The solid state series of linear constant current regulators (CCRs) are <u>Simple</u>, <u>E</u>conomical and <u>R</u>obust (SER) devices designed to provide a cost effective solution for regulating current in LEDs. The SER series regulators are based on patent-pending Self-Biased Transistor (SBT) technology and regulate current over a wide range of voltage. The series is designed with a negative temperature coefficient to protect LEDs from thermal runaway at extreme voltage and operating temperature.

The SER series of constant current regulators require no external components and their high anode-cathode voltage rating withstands surges common in Automotive, Industrial and Commercial Signage Applications. The SER series is available in thermally robust packages and is qualified to stringent AEC 101 standard. The SER series devices are lead-free ROHS compliant and use halogen-free molding compound.

## Applications

- Refer to Application Note AND8391/D for Power Dissipation Consideration
- Refer to Application Note AND8349/D for Automotive Center High Mount Stop Lamp (CHMSL) Application
- Automotive Chevron Side Mirror Markers, Cluster, Display & Instrument Backlighting
- Switch Contact Wetting
- Display Signage
- Channel Letters
- AC Lighting Panels
- Decorative Lighting

## Features

- This Device uses Halogen-Free Molding Compound
- This is a Pb–Free Device

### **MAXIMUM RATINGS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit	
Anode-Cathode Voltage	Vak Max	45	V	
Reverse Voltage	V <sub>R</sub>	500	mV	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C	
ESD Rating: Human Body Model Machine Model	ESD	Class 1C Class B		

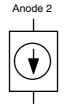
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



## **ON Semiconductor®**

http://onsemi.com

l<sub>reg(SS)</sub> = 25 mA @ Vak = 7.5 V

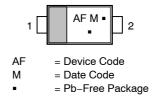


Cathode 1



CASE 425 STYLE 1





(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSI45025AT1G	SOD-123 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Steady State Current @ Vak = 7.5 V (Note 1)	I <sub>reg(SS)</sub>	22.5	25	27.5	mA
Voltage Overhead (Note 2)	Voverhead		1.8		V
Pulse Current @ Vak = 7.5 V (Note 3)	I <sub>reg(P)</sub>	26.1	29.4	32.75	mA

 1.  $I_{reg(SS)}$  steady state is the voltage (Vak) applied for a time duration  $\geq$  10 sec, using FR-4 @ 300 mm<sup>2</sup> 1 oz. Copper traces, in still air.

 2.  $V_{overhead} = V_{in} - V_{LEDs}$ .  $V_{overhead}$  is minimum value for 85%  $I_{reg(SS)}$ .

 3.  $I_{reg(P)}$  non-repetitive pulse test. Pulse width t  $\leq$  300  $\mu$ sec.

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 4) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	208 1.66	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 4)	$R_{ heta JA}$	600	°C/W
Thermal Reference, Junction-to-Cathode Lead (Note 4)	$R_{\psi JL}$	196	°C/W
Total Device Dissipation (Note 5) T <sub>A</sub> = 25°C Derate above 25°C	PD	227 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 5)	$R_{ extsf{ heta}JA}$	550	°C/W
Thermal Reference, Junction-to-Cathode Lead (Note 5)	R <sub>ψJL</sub>	160	°C/W
Total Device Dissipation (Note 6) $T_A = 25^{\circ}C$ Derate above 25°C	PD	347 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 6)	$R_{ heta JA}$	360	°C/W
Thermal Reference, Junction-to-Cathode Lead (Note 6)	R <sub>ψJL</sub>	160	°C/W
Total Device Dissipation (Note 7) $T_A = 25^{\circ}C$ Derate above 25°C	PD	368 2.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 7)	$R_{ extsf{ heta}JA}$	340	°C/W
Thermal Reference, Junction-to-Cathode Lead (Note 7)	R <sub>ψJL</sub>	132	°C/W
Total Device Dissipation (Note 8) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	436 3.5	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 8)	R <sub>θJA</sub>	287	°C/W
Thermal Reference, Junction-to-Cathode Lead (Note 8)	R <sub>ψJL</sub>	148	°C/W
Total Device Dissipation (Note 9) $T_A = 25^{\circ}C$ Derate above 25°C	PD	463 3.7	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 9)	$R_{ heta JA}$	270	°C/W
Thermal Reference, Junction-to-Cathode Lead (Note 9)	R <sub>ψJL</sub>	120	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

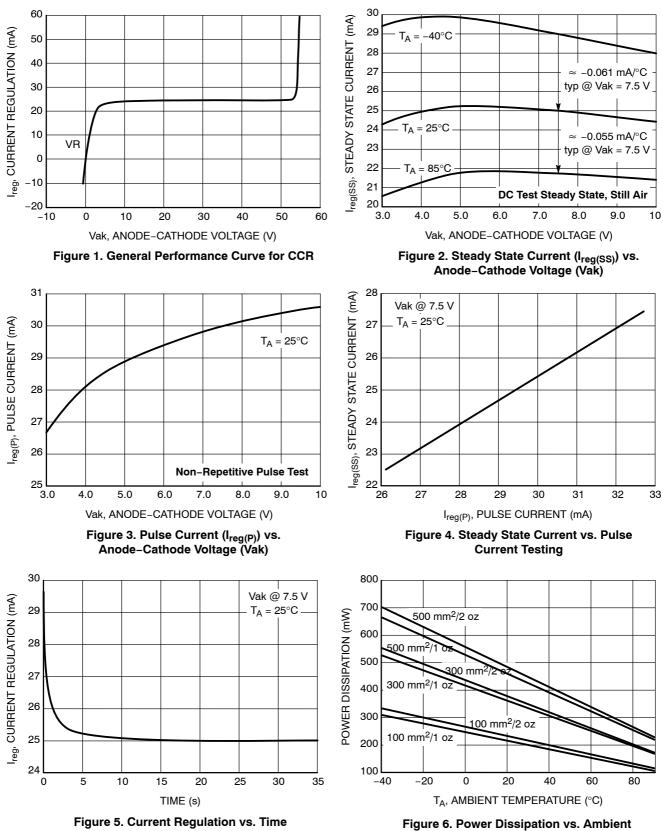
FR-4 @ 100 mm<sup>2</sup>, 1 oz. copper traces, still air.
 FR-4 @ 100 mm<sup>2</sup>, 2 oz. copper traces, still air.
 FR-4 @ 300 mm<sup>2</sup>, 1 oz. copper traces, still air.
 FR-4 @ 300 mm<sup>2</sup>, 2 oz. copper traces, still air.
 FR-4 @ 500 mm<sup>2</sup>, 1 oz. copper traces, still air.
 FR-4 @ 500 mm<sup>2</sup>, 0 oz. copper traces, still air.

9. FR-4 @ 500 mm<sup>2</sup>, 2 oz. copper traces, still air.

NOTE: Lead measurements are made by non-contact methods such as IR with treated surface to increase emissivity to 0.9. Lead temperature measurement by attaching a T/C may yield values as high as 30% higher °C/W values based upon empirical measurements and method of attachment.

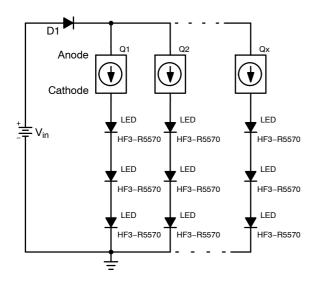
## **TYPICAL PERFORMANCE CURVES**

Minimum FR-4 @ 300 mm<sup>2</sup>, 1 oz Copper Trace, Still Air



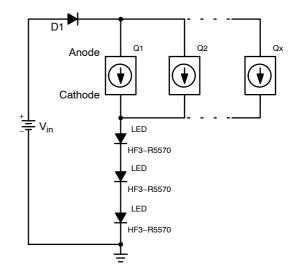
Temperature @ T<sub>J</sub> = 150°C

## APPLICATIONS



## Figure 7. Typical Application Circuit (25 mA each LED String)

Number of LED's that can be connected is determined by: D1 is a reverse battery protection diode LED's =  $((V_{in} - Q_X V_F + D1 V_F)/LED V_F)$ Example:  $V_{in} = 12 Vdc$ ,  $Q_X V_F = 3.5 Vdc$ , D1VF = 0.7 VLED  $V_F = 2.2 Vdc$  @ 25 mA (12 Vdc - 4.2 Vdc)/2.2 Vdc = 3 LEDs in series.

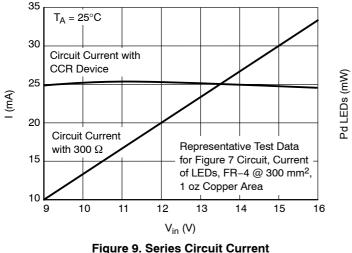


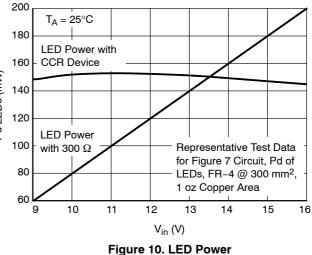
## Figure 8. Typical Application Circuit (75 mA each LED String)

Number of LED's that can be connected is determined by: D1 is a reverse battery protection diode Example:  $V_{in} = 12$  Vdc,  $Q_X V_F = 3.5$  Vdc, D1VF = 0.7 V LED  $V_F = 2.6$  Vdc @ 75 mA (12 Vdc - (3.5 + 0.7 Vdc))/2.6 Vdc = 3 LEDs in series. Number of Drivers = LED current/25 mA 75 mA/25 mA = 3 Drivers (Q1, Q2, Q3)

ON Semiconductor CCR Design	Resistor Biased Design		
Constant brightness over full Automotive Supply Voltage (more efficient), see Figure 9	Large variations in brightness over full Automotive Supply Voltage		
Little variation of power in LEDs, see Figure 10	Large variations of current (power) in LEDs		
Constant current extends LED strings lifetime, see Figure 9	High Supply Voltage/ Higher Current in LED strings limits lifetime		
Current decreases as voltage increases, see Figure 9	Current increases as voltage increases		
Current supplied to LED string decreases as temperature increases (self-limiting), see Figure 2	LED current decreases as temperature increases		
No resistors needed	Requires costly inventory (need for several resistor values to match LED intensity)		
Fewer components, less board space required	More components, more board space required		
Surface mount component	Through-hole components		

## Comparison of LED Circuit using CCR vs. Resistor Biasing



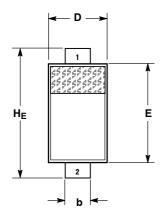


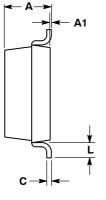
# Current Regulation: Pulse Mode $(I_{reg(P)})$ vs DC Steady-State $(I_{reg(SS)})$

There are two methods to measure current regulation: Pulse mode  $(I_{reg(P)})$  testing is applicable for factory and incoming inspection of a CCR where test times are a minimum. (t  $\leq$  300 µs). DC Steady-State  $(I_{reg(SS)})$  testing is applicable for application verification where the CCR will be operational for seconds, minutes, or even hours. ON Semiconductor has correlated the difference in  $I_{reg(P)}$  to  $I_{reg(SS)}$  for stated board material, size, copper area and copper thickness.  $I_{reg(P)}$  will always be greater than  $I_{reg(SS)}$  due to the die temperature rising during  $I_{reg(SS)}$ . This heating effect can be minimized during circuit design with the correct selection of board material, metal trace size and weight, for the operating current, voltage, board operating temperature (T<sub>A</sub>) and package. (Refer to Thermal Characteristics table).

### PACKAGE DIMENSIONS

**SOD-123** CASE 425-04 ISSUE E





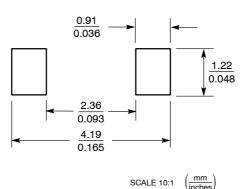
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI

Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.94	1.17	1.35	0.037	0.046	0.053
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.51	0.61	0.71	0.020	0.024	0.028
С			0.15			0.006
D	1.40	1.60	1.80	0.055	0.063	0.071
E	2.54	2.69	2.84	0.100	0.106	0.112
HE	3.56	3.68	3.86	0.140	0.145	0.152
L	0.25			0.010		
STYLE 1:						

PIN 1. CATHODE 2. ANODE

#### SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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