



FEATURES

- RoHS compliant
- Industry standard eighth-brick pinout and package
- Outputs from 1.5V to 12V up to 100W
- Low profile 0.4" height with 0.9" x 2.3" outline dimensions
- 36 to 75 Vdc nominal input
- Fully isolated, 2250 Vdc (BASIC) insulation
- Outstanding thermal performance and derating
- Extensive self-protection and short circuit features with no output reverse conduction
- On/Off control, trim and sense functions
- Interleaved synchronous rectification yields high efficiency over 90%
- Fully protected against temperature and voltage limits
- UL/EN/IEC 60950-1 safety approvals
- Qual/HALT/EMI testing is scheduled

For efficient, fully isolated DC power in the smallest space, the UCE open frame DC/DC converter series fit in industry-standard "eighth brick" outline dimensions and mounting pins (on quarter-brick pinout).

PRODUCT OVERVIEW

Units are offered with fixed output voltages from 1.5 to 12 Volts and currents up to 40 Amps. UCEs operate over a wide temperature range (up to +85 degrees Celsius at moderate airflow) with full rated power. Interleaved synchronous rectifier topology yields excellent efficiency over 90% and no reverse output conduction.

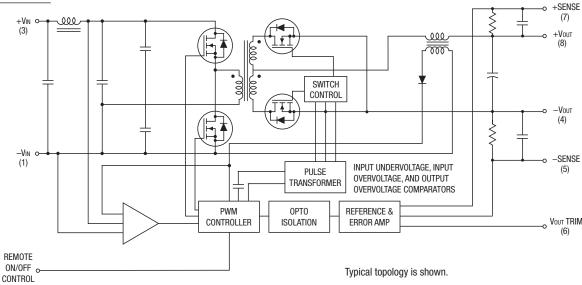
UCE's achieve these impressive mechanical and environmental specs while delivering excellent electrical performance in a through-hole package. Overall noise is typically 50 mV pk-pk (low voltage models) with fast step response. These converters offer tight output regulation and high stability even with no load. The unit is fully protected against input undervoltage, output overcurrent and short circuit. An on-board temperature sensor shuts

down the converter if thermal limits are reached. "Hiccup" output protection automatically restarts the converter when the fault is removed.

A convenient remote On/Off control input enables phased startup and shutdown in multi-voltage applications. To compensate for longer wiring and to retain output voltage accuracy at the load, UCEs employ a Sense input to dynamically correct for ohmic losses. A trim input may be connected to a user's adjustment potentiometer or trim resistors for output voltage calibration. The UCE will tolerate substantial capacitive loading for bypass-cap applications.

UCEs include industry-standard safety certifications and BASIC I/O insulation provides input/output isolation to 2250V. Radiation emission testing is performed to widely-accepted EMC standards.

SIMPLIFIED BLOCK DIAGRAM

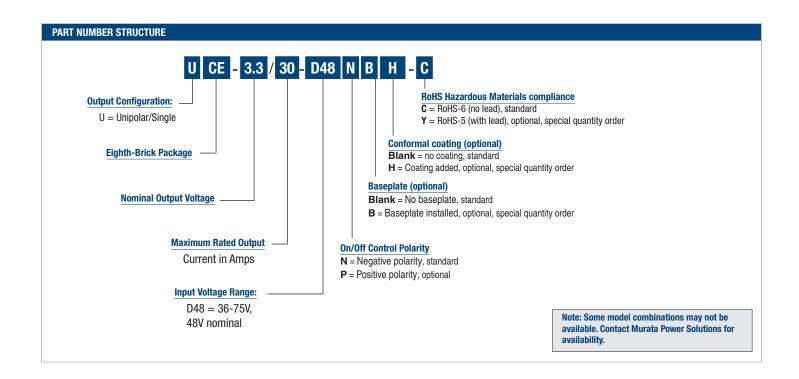








PERFORMANCE SPECIF	PERFORMANCE SPECIFICATIONS AND ORDERING GUIDE														
Output								Input							
	V out	Іоит	Power		& Noise p-p)	Regu	lation	V _{IN} Nom.	Range	lın, no load	lın, full load	Effic	iency	Pac	kage
Model Family	(V)	(A)	(W)	Тур.	Max.	Line	Load	(V)	(V)	(mA)	(A)	Min.	Тур.	Case	Pinout
UCE-1.2/40-D48N-C	1.2	40	48				Please contac	t Murata Po	ower Solut	ions for fu	rther inform	nation.			
UCE-1.5/20-D48N-C	1.5	20	30	50	100	±0.15%	±0.3%	48	36-75	50	0.72	85%	87%	C56	P32
UCE-1.5/40-D48N-C	1.5	40	60				Please contac	t Murata Po	ower Solut	ions for fu	rther inform	nation.			
UCE-1.8/30-D48N-C	1.8	30	54	30	80	±0.125%	±0.25%	48	36-75	45	1.28	87%	88%	C56	P32
UCE-2.5/20-D48N-C	2.5	20	50				Please contac	t Murata D	ower Colut	iono for fu	rthor inform	notion			
UCE-2.5/40-D48N-C	2.5	40	100				riease contac	i iviui ala Fi	ower solut	10115 101 1U	i uiei iiiioiii	iauoii.			
UCE-3.3/15-D48N-C	3.3	15	49.5	50	100	±0.125%	±0.25%	48	26.75	60	1.15	86%	90%	C56	P32
UCE-3.3/30-D48N-C	3.3	30	99	50	100	±0.1%	±0.2%	40	36-75	60	2.27	89%	91%	U36	P32
UCE-5/10-D48N-C	5	10	50				Please contac	t Murata D	ower Colut	iono for fu	rthor inform	notion			
UCE-5/20-D48N-C	5	20	100				rivase contac	i wiliala Pi	ower Solut	101 61 1U	i uiei illioili	iauvii.			
UCE-12/4.2-D48N-C	12	4.2	50.4	150	300	±0.125%	±0.25%	48	36-75	6-75 50	1.14	86%	92%	C56	P32
UCE-12/8.3-D48N-C	12	8.3	99.6	200	300	±0.125%		48	30-75		2.31	0070	90%	030	F 32





INPUT CHARACTER	INPUT CHARACTERISTICS														
	Start-u			Reflected			Inpu	t Current					Re	mote On/Off	Control
Model Family	V _{IN} (Volts)	thresh- old Min. (A)	voltage Shut- down (V)	(back) Ripple Current (mA)	Full Load Condi- tions	Inrush Tran- sient A ² sec	Output Short Circuit (mA)	No Load (mA)	Low Line (Vin=min.) (A)	Standby Mode (mA)	Input	Reverse Polarity Protec- tion		Positive Logic "P" Model Suffix	Negative Logic "N" Model Suffix
UCE-1.5/20-D48	, ,	. ,	32	, ,			. ,	. ,	0.97				, ,	OEE_Cround	OFF=open or
UCE-1.8/30-D48			32.5		_		50-150, model	45-60, model nt dependent	1.72	1-10, model L-C dependent			pin to	+2.5V to	
UCE-3.3/15-D48	10	32 32 32 32	32	10-30,	ordering $\frac{0.05}{\Delta^2 \text{sec}}$	0.05			1.54		l C	See	1.0	+1V max.	
UCE-3.3/30-D48	40		32	model dependent		A ² sec			3.06		notes		ON=open or	ON=Ground	
UCE-12/4.2-D48			32		3	140	aoponaont		1.52						pin to +0.8V
UCE-12/8.3-D48			32						3.07					max.	max.

OUTPUT CHARACT	OUTPUT CHARACTERISTICS										
Model Family	Vout V	Vout Accuracy 50% Load % of Vnom	Capacitive Loading Max. Low ESR <0.02Ω Max. resistive load μF	Adjustment Range	Temperature Coefficient	Minimum Loading	Remote Sense Compen- sation	Ripple/ Noise (20 MHz bandwidth)	Line/Load Regulation	Efficiency	Current Limit Inception 98% of Vout, after warmup A
UCE-1.5/20-D48	1.5		10,000				imum +10%			24.5	
UCE-1.8/30-D48	1.8		10,000								36
UCE-3.3/15-D48	3.3	±1%	10,000 max.	-10 to +10% of	±0.02% of Vout range	No minimum		e,	e ordering qui	do	24
UCE-3.3/30-D48	3.3	±1%	10,000	Vnom.	per °C	load	+1070	36	e ordering gun	ering guide	35
UCE-12/4.2-D48	12		1000	- VIIOIII.	po. 0						5.5
UCE-12/8.3-D48	12		1000								12

ISOLATION CHARACTERISTICS									
Model Family	Input to Output Min. V	Input to baseplate Min. V	Baseplate to output Min. V	Isolation Resistance MΩ	Isolation Capacitance pF	Isolation Safety Rating			
UCE-1.5/20-D48				100					
UCE-1.8/30-D48			1500	10					
UCE-3.3/15-D48	2250	1500		100	1000	Basic Insulation			
UCE-3.3/30-D48	2250	1500	1500		1000	Dasic ilisulation			
UCE-12/4.2-D48				100					
UCE-12/8.3-D48									

MISCELLANEOUS C	HARACTERIS	TICS								
Model Family	Calculated MTBF ⁴	Operating Temperature Range with derating (°C)	Operating Case Temperature (no derating)	Storage Temperature Range (°C)	Thermal Protection/ Shutdown (°C)	Short Circuit Current (A)	Overvoltage Protection ¹² (V) Via magnetic feedback (V)	Short Circuit Protection Method	Short Circuit Duration ¹⁶	Relative Humidity (non-condensing)
UCE-1.5/20-D48					120	5	1.95	Current	Continuous, output shorted to	
UCE-1.8/30-D48		TBC -40 to +85 -40 to +120					2.8 V. max	limiting, hiccup autorestart.		
UCE-3.3/15-D48	TDC		40 to +120	-55 to	120		4.25			to +85°C/85%
UCE-3.3/30-D48	100		+125		ິນ	4.20	Remove	ground. No	10 +05 6/05/0	
UCE-12/4.2-D48 UCE-12/8.3-D48					125		14.5	overload for recovery.	d for damage.	





DYNAMIC CHARACTERISTICS								
		Start-						
	Dynamic Load Response (50-75-50%	VIN to Vout regulated (Max.)	Remote On/ Off to Vout regulated (Max.)	Switching Frequency				
Model Family	load step)	m	KHz					
UCE-1.5/20-D48				480				
UCE-1.8/30-D48	30-200 µSec			400 ±40				
UCE-3.3/15-D48	to ±1% of final	5-50, model	5-50, model	480 ±50				
UCE-3.3/30-D48	value, model	dependent	dependent	380 ±40				
UCE-12/4.2-D48	dependent			200 ±10				
UCE-12/8.3-D48				200 ±10				

ABSOLUTE MAXIMUM RATINGS	
Input Voltage: Continuous: 48 Volt input models Transient (100 mSec. Max.)	75 Volts
48 Volt input models	100 Volts
On/Off Control	+15 Volts
Input Reverse Polarity Protection	5 Amps, 10 sec. max.
Output Overvoltage Protection	Magnetic feedback. See specifications.
Output Current *	Current-limited. Devices can withstand sustained short circuit without damage.
Storage Temperature	-40 to +125°C.
Lead Temperature	+280°C, 10 seconds max.
Absolute maximums are stress ratings. Expos	sure of devices to any of these conditions

Note: Not all model combinations are available.

PERFORMANCE SPECIFICATION NOTES

(1) All models are tested and specified with external 1||10 μF ceramic/tantalum output capacitors and no external input capacitor. All capacitors are low ESR types. These capacitors are necessary to accommodate our test equipment and may not be required to achieve specified performance in your applications. All models are stable and regulate within spec under no-load conditions.

General conditions for Specifications are +25 deg.C, $V_{IN} = nominal$, $V_{OUT} = nominal$, full load. Adequate airflow must be supplied for extended testing under power.

- (2) Input Ripple Current is tested and specified over a 5 Hz to 20 MHz bandwidth. Input filtering is $C_{IN}=33~\mu F$, 100V tantalum, $C_{BUS}=220~\mu F$, 100V electrolytic, L_{BUS}=12 μH .
- (3) Note that Maximum Power Derating curves indicate an average current at nominal input voltage. At higher temperatures and/or lower airflow, the DC/DC converter will tolerate brief full current outputs if the total RMS current over time does not exceed the Derating curve. All Derating curves are presented at sea level altitude. Be aware of reduced power dissipation with increasing density altitude.
- (4) Mean Time Before Failure is calculated using the Telcordia (Belcore) SR-332 Method 1, Case 3, ground fixed conditions, Tpcboard=+25 deg.C, full output load, natural air convection
- (5) The On/Off Control is normally controlled by a switch. But it may also be driven with external logic or by applying appropriate external voltages which are referenced to Input Common. The On/Off Control Input should use either an open collector or open drain transistor.
- (6) Short circuit shutdown begins when the output voltage degrades approximately 2% from the selected setting.

(7) The outputs are not intended to sink appreciable reverse current. This may damage the outputs.

may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor

- (8) Output noise may be further reduced by adding an external filter. See I/O Filtering and Noise Reduction.
- (9) All models are fully operational and meet published specifications, including "cold start" at -40° C.
- (10) Regulation specifications describe the deviation as the line input voltage or output load current is varied from a nominal midpoint value to either extreme.
- (11) Alternate pin length and/or other output voltages are available under special quantity order
- (12) Output current limit is non-latching. When the overcurrent fault is removed, the converter will immediately recover.
- (13) Do not exceed maximum power specifications when adjusting the output trim.
- (14) At zero output current, the output may contain low frequency components which exceed the ripple specification. The output may be operated indefinitely with no load.
- (15) If reverse polarity is accidentally applied to the input, a body diode will become forward biased and will conduct considerable current. To ensure reverse input protection with full output load, always connect an external input fuse in series with the $+V_{IN}$ input. Use approximately twice the full input current rating with nominal input voltage.



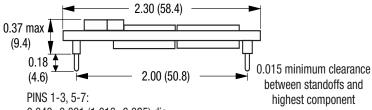
PHYSICAL CI	HARACTERISTICS	
Outline dimensi	ons	See mechanical specs (below)
Pin material		Copper alloy
Pin diameter		0.04/0.062" (1.016/1.524mm)
Pin finish		Nickel underplate with gold overplate
	UCE-1.5/20-D48	0.67 ounces (19 grams)
Mainh	UCE-1.8/30-D48, UCE-12/4.2-D48	0.71 ounces (20 grams)
Weight	UCE-3.3/15-D48	1 ounce (28 grams)
	UCE-3.3/30-D48, UCE-12/8.3-D48	0.81 ounces (23 grams)
Electromagnetic interference (conducted and radiated) (external filter required)		FCC part 15, class B, EN55022
Safety		UL/cUL 60950-1, CSA-C22.2 No. 60950-1, IEC/EN 60950-1





MECHANICAL SPECIFICATIONS

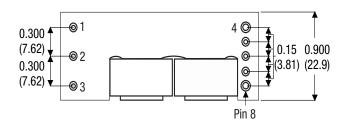
Without Baseplate



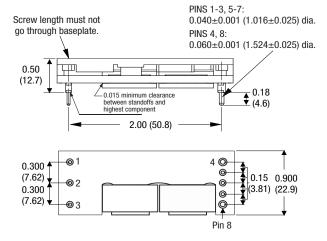
 $0.040\pm0.001~(1.016\pm0.025)$ dia.

PINS 4, 8:

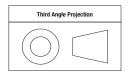
0.060±0.001 (1.524±0.025) dia.



With Baseplate



Dimensions are in inches (mm shown for ref. only).



Tolerances (unless otherwise specified): .XX \pm 0.02 (0.5) .XXX \pm 0.010 (0.25)

Angles ± 2°

Components are shown for reference only.

INPUT/OUTPUT CONNECTIONS

Pin Function P32

Pin	Function P32
1	-Input
2	On/Off Control
3	+Input
4	-Output
5	-Sense
6	Output Trim
7	+Sense
8	+Output

Dimensions are in inches (mm). Typical component locations are shown. Actual units may vary.



Trim Equations

Trim Down

Connect trim resistor between trim pin and –Sense

Trim Up

Connect trim resistor between trim pin and +Sense

$$R_{TrimDn} (k \Omega) = \frac{5.11}{\Delta} - 10.22$$

$$R_{\text{TrimUp}} (k \Omega) = \frac{5.11 \times \text{V}_{\text{NOM}} \times (1 + \Delta)}{1.225 \times \Delta} - \frac{5.11}{\Delta} - 10.22$$

Where,

 $\Delta = \mid \text{(Vnom} - \text{Vout)} \, / \, \text{Vnom} \mid$

VNOM is the nominal, untrimmed output voltage.

Vout is the desired new output voltage.

Do not exceed the specified trim range or maximum power ratings when adjusting trim. Use 1% precision resistors mounted close to the converter on short leads.

Trim Circuits

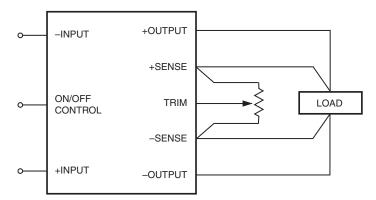


Figure A. Trim Connections Using A Trimpot

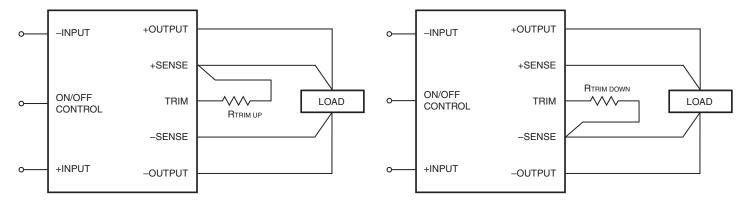
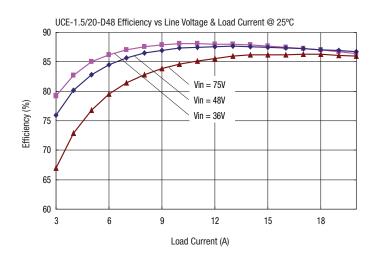
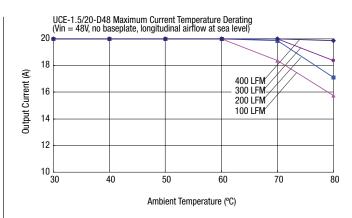


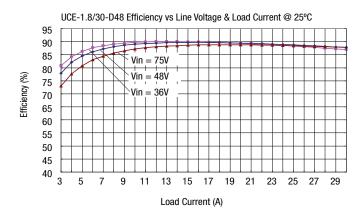
Figure B. Trim Connections To Increase Output Voltages

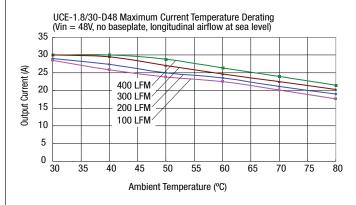
Figure C. Trim Connections To Decrease Output Voltages

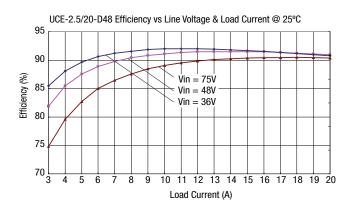
Typical Performance Curves

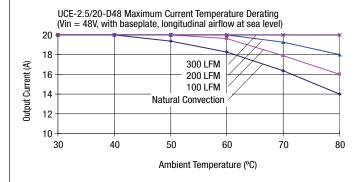




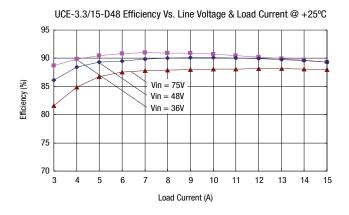


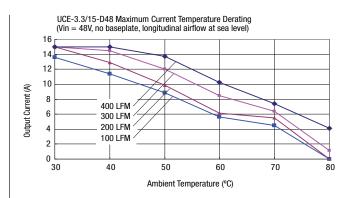


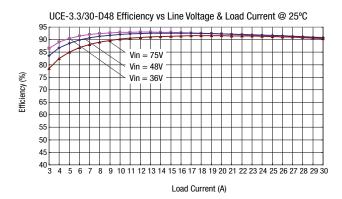


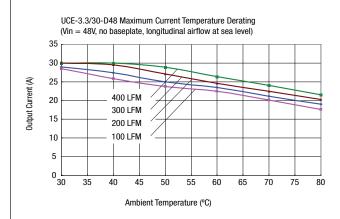


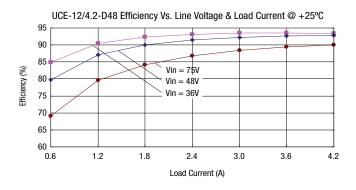
Typical Performance Curves

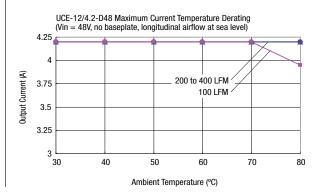






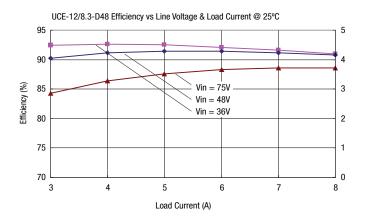


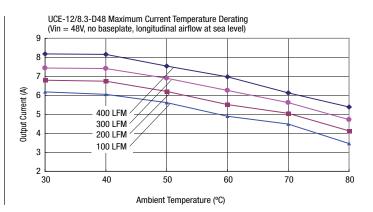






Typical Performance Curves







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